



Mark Dapore
Chief Technologist

Mark Dapore is Engineering Fellow and Chief Technologist at L3Harris. He is responsible for the technology planning and new product development for Space products at Space and Sensor's division of L3Harris. In his customer facing responsibilities, he builds technology roadmaps and secures funding to meet future space market needs.

Mr. Dapore previously served as General Manager of Space Avionics division, responsible for financial performance and strategic focus in Space products. The Space group designs and manufactures launch vehicle control avionics, command receivers, transponders, telemetry transmitters, payload transmitters and

data acquisition units flying on the earth orbit satellites, deep space satellites, and launch vehicles. L3Harris is also extending its infrared sensor business into space to include earth imaging, missile detect and space situational awareness.

Mr. Dapore has also served as Vice President of Engineering and was responsible for product R&D, advanced product development, product upgrades, and engineering support across both Space and EO/IR business lines. He previously served as the Director of Engineering for Space Avionics, successfully managing design and development efforts. During his tenures as Space Engineering Manager and Director of

Engineering, the Space Engineering group grew from 15 to more than 80 engineers.

In 1983, Mr. Dapore joined L3 Space & Sensors as a design engineer specializing in RF system design, amplifiers, filters, modems, FPGAs, and ASICs for spacecraft and launch vehicle avionics. He also led technology development in digital signal processing and embedded software/firmware. Prior to joining L3, Mr. Dapore was employed by the University of Dayton Research Institute and was responsible for the Electronic Components Testing Lab. He was responsible for design of test fixturing and developing test plans for high-reliability components.

Mr. Dapore has a Master of Science degree in Electrical Engineering from the University of Cincinnati with a major thrust in signal processing, and holds a Bachelor of Technology degree in Electronics Engineering from the University of Dayton.